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Education:

- **Ph.D.- Electronic Engineering.**
Department of Electronic Engineering, Graduate School of Engineering,
The University of Tokyo,
Japan,
September-2006.
- **M.Sc.- Electronic Engineering.**
Department of Electrical Engineering, Faculty of Engineering,
Assiut University,
Egypt,
August-1996.
- **B.Sc."Distinction with Honor" - Electronics & Communications Engineering,**
Department of Electrical Engineering, Faculty of Engineering,
Assiut University,
Egypt,
July-1992.

Working History:

- *Mar./2013 - Present:* Associate professor, Electrical Engineering Department,
Faculty of Engineering, Assiut University, Egypt
- *Oct./2010 - Feb./2012:* Assistant professor, Electrical Engineering Department,
Faculty of Engineering, Assiut University, Egypt.
- *Aug./2008-Sept./2010:* Project Researcher at Advantest Design to Test
(D2T) research division, VLSI Design and Education Center (VDEC), The
University of Tokyo, Japan
- *Nov./2006 - Jul./2008:* Assistant professor, Electrical Engineering Department,
Faculty of Engineering, Assiut University, Egypt.
- *Mar./2003-Sept./2006:* PhD Student, Graduate School of Engineering, The
University of Tokyo, Tokyo, Japan (Through scholarship supported by Egyptian
government).

- *Oct./2002-Feb./2003*: Visiting researcher, VLSI Design and Education Center (VDEC), The University of Tokyo, Tokyo Japan.
- *Mar./1993-Sept./2002*: Assistant Lecturer and lecturer Associate, Department of Electrical Engineering, Faculty of Engineering, Assiut University.

Teaching Experiences:

- **Nov./2006 –Jul./2008, Oct./ 2010 to Present**
 - **Assistant/Associate professor**, Electrical Engineering Department, Faculty of Engineering, Assiut University, Egypt. I had been responsible about
 - (i) teaching the following courses to undergraduate students:-**
 - Electronics (1), 2012/2013
 - Electronics (2), 2012/2013, 2013/2014
 - Electric Circuit Analysis, 2012/2013
 - Introduction to VLSI Design, (2007/2008, 2010/2011, 2011/2012, 2012/2013).
 - Digital Systems Design using VHDL, (2011/2012, 2012/2013, 2013/2014).
 - Electric Circuit Analysis to Mechatronics Division Students, (2007/2008).
 - Electric and Electronic Circuits Analysis to Computers and Information College students, (2010/2011).
 - Electronic Circuits to Mechatronic Division Students, (2007/2008).
 - Electrical Properties of Materials, (2011/2012).
 - (ii) teaching the following courses to postgraduate students:-**
 - Data Converters (2013/2014).
 - Digital Circuits Design Using VHDL (2010/2011).
 - Advanced Topics in Electronic Circuits Design. (2007/2008)
 - Applied Electronics –for Mechanical Eng. Dept. Students (2007/2008).
 - (iii) supervising the following:-**
 - Electrical Testing labs of 1st and 2nd grade Electrical Engineering Student, (2007/2008, 2010/2011, 2011/2012, 2012/2013, 2013/2014).
 - Electrical Testing labs of 3rd and 4rd grade Electronics and Communications Section Students, (2007/2008, 2010/2011, 2011/2012).

- Senior Design Graduation Project, RFID-Based Security System Design and Implementation (2013/2014).
- Senior Design Graduation Project, Design of Low-Power ALU (2013/2014).
- Senior Design Graduation Project, Design and Implementation of Optical Fiber Communication systems (2010/2011,2011/2012)
- Senior Design Graduation Project, FPGA and ASIC Implementation of a Local Positioning System (LPS) (2007/2008).
- Senior Design Graduation Project, Secured Home Automation (2007/2008).

(iii) Member of the following committees:-

- Quality and Accreditation of EE Dept. Program.
 - Developing of Post-graduate and Research program of EE Dept.
 - EE Dept. council.
- **Adjunct Assistant Professor**, Electrical Engineering Department, Faculty of Engineering, Sohag University, Egypt. I had been responsible about teaching the following courses:-
 - Logic Design, 1st Semester 2010/2011
 - Electronics (2), 2nd Semester 2012/2013
 - Electrical Measurements, 1st Semester 2012/2013
 - **Adjunct Assistant Professor**, Electrical Engineering Department, Faculty of Engineering, South Valley University, Egypt. I had been responsible about teaching the following courses:-
 - Electronics (3) , 1st Semester 2007/2008
 - Electronic Circuits (2), 2nd Semester 2007/2008
 - Electrical Measurements, 1st Semester 2012/2013
- **Mar./1993 – Sept./2002:**
 - **Assistant Lecturer**, Department of Electrical Engineering, Faculty of Engineering, Assiut University, Egypt. I was the teaching assistant of the following courses and graduation projects:
 - Electronics (1)
 - Electronics (2)
 - Electronics (3)
 - Engineering Analysis

- Electrical Properties of Materials
- Communications Theory
- Electronic Measurements and Instrumentation
- Electrical Testing.
- Photovoltaic Power Generation (Graduation Project)
- PC to TV Interfacing (Graduation Project)
- Telecommunications Electronic Switch (Graduation Project)
- PC LAN. (Graduation Project)

Research and Supervision Experience:

- **Research Interests:**

- Design of low-cost ADCs.
- Designing of high-reliability low-power VLSI circuits and systems.
- Testing and design for testability of high-speed mixed signal systems.
- Design, Modeling and Characterization of MEMS resonators for Biomedical Applications (Just started)
- Digitally-Assisted Analog Design (Just Started)
- Semiconductor device modeling and applications.

- **M.Sc. Thesis Supervision:**

- Mohamed Zanaty, “Design and modeling of Nano / Micro electromechanical resonators for biosensing applications”, Electrical Engineering Department, Faculty of Engineering, Assiut University, Egypt , 2013.
- Ahmed Khaled, “Characterization of bio-coated MEM resonators for molecular sensing applications”, Electrical Engineering Department, Faculty of Engineering, Assiut University, Egypt , 2013.
- Kasem Khalil, “Design of comparators and high performance TDCs for level-crossing ADCs”, Electrical Engineering Department, Faculty of Engineering, Assiut University, Egypt, 2014.

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- Mohamed Mahrous, “Techniques of minimizing leakage power in CMOS circuits in nano-scale technologies”, Electrical Engineering Department, Faculty of Engineering, Assiut University, Egypt (Running)
- Ashraf Ramadan, “Cost-effective test methodologies for pipelined ASCs”, Electrical Engineering Department, Faculty of Engineering, Assiut University, Egypt (Running).

Professional Presentations and Invited Talks

- [1] "Analog/Mixed Signal Design: Challenges, Techniques and Opportunities" Half-Day tutorials, IEEE JEC-ECC, Alexandria. Egypt Dec. 2013.
- [2] “A Low-Power Low-Delay Dispersion Comparator for High-Speed Level-Crossing ADCs”, IEEE 2nd Saudi International Electronics, Communications and Photonics Conference SIECPC2013, April, 2013, Riyadh, KSA
- [3] “Novel Technique for Reducing the Comparator Delay Dispersion in 45nm CMOS Technology for Level-Crossing ADCs”, IEEE ISCDG2012, Sept. 2012, Grenoble- France
- [4] ”Low Delay Dispersion Comparator for Level-Crossing ADCs”, IEEE JECECC2011, March-2011, Alexandria, Egypt.
- [5] “Fault Detection and Diagnoses Methodology for Adaptive Digitally-Calibrated Pipelined ADCs”, IEEE IDT2011, Beirut, Lebanon, Dec. 2011
- [6] **Poster** ” Novel Technique for Minimizing the Comparator Delay Dispersion in 65nm CMOS Technology”, IEEE ICECS2011, Beirut, Lebanon, Dec. 2011
- [7] **Invited Talk**, “High-speed clocked comparator for on-chip signal monitoring applications”, Advantest D2T Symposium, VLSI Design and Education Center (VDEC), The University of Tokyo, Japan, Match 2011.
- [8] **Invited Talk** “An Automatic Test Generation Framework for Adaptive Mixed-Signal Systems” Advantest Research and Development Laboratory, Gunma, Japan January 2010.
- [9] **Invited Talk**, “Cost-Effective Test Methodology for Analog and Mixed-Signals in SoCs”, D2T Symposium, VDEC. The university of Tokyo, Japan, June 2010.

- [10] "Clocked Comparator for High-Speed Applications in 65nm Technology", A-SSCC2010, China, Beijing, November 2010.
- [11] "An Automatic Test Generation Framework for Digitally-Assisted Adaptive Equalizers in High-Speed Serial Links ", DATE2010, Germany, Dresden, March-2009.
- [12] "Signature-Based Testing for Adaptive Digitally-Calibrated Pipelined Analog-to-Digital Converters", IEEE ASICON09, Changsha, China October-2009.
- [13] "GA-Based Test Generation for Digitally-Assisted Adaptive Equalizers in High-Speed Serial Links", IEEE EWDTS, Moscow, Russia, September-2009.
- [14] "Signature-Based Testing for Digitally-Assisted Adaptive Equalizers in High-Speed Serial Links", IEEE European Test Symposium, ETS09, Seville, Spain, May-2009.
- [15] "On-chip 8GHz Non-Periodic High-Swing Noise Detector" Proceedings of DATE 2006 , Germany, Munich, March-2006
- [16] "Statistical Model for Logic Errors in CMOS Digital Circuits for Reliability-Driven Design Flow" DDECS06, Czech Republic, Prague, April-2006.
- [17] "On-chip Detector for Non-Periodic High-Swing Noise Detection" IEEE Int. SoC Design Conf. pp: 231-234, S. Korea, Seoul, October-2005.
- [18] "On-chip Non-Periodic High-Swing Noise Detector" 12th IEEE Int. Conf. on Electronics, Circuits and System, Tunisia , Tunis, December 2005.
- [19] "Noise Effects on Performance of Low Power Design Schemes in Deep Submicron Regime" 19th IEEE Int. Symp. On Defect and Fault Tolerance in VLSI, France, Cannes, October-2004.
- [20] "On High Noise Immunity CMOS Design Scheme with Low Leakage Power Consumption", 7th Int. Conf. on Solid-State and Integrated Circuit Technology, China, Beijing, October-2004.
- [21] "Statistical Evaluation of Logic Errors in Low Power Design Schemes", IEICE national Conf. 2004, Japan 2004
- [22] "On-chip Detector for Non-Periodic High-Swing Noise Sensing", IEICE national Conf. 2005, Japan 20~23-Sept. 2005.

Professional Training:

- "Preparation of Lecturer", Assiut University, Egypt.

- “Training of Trainee” , Assiut University, Egypt.
- “Ethics of Academic Careers” Assiut University, Egypt.
- “Course Preparation” , Assiut University, Egypt.
- “Scientific Publishing”, Assiut University, Egypt.
- “Preparation of Scientific Conferences”, Assiut University, Egypt.
- “Management of Research Team” Assiut University, Egypt
- “Skills of e-learning”, Almajmaah University, Kingdom of Saudi Arabia.

Awards and Patents:

- **Best paper award** of IEEE JEC-ECC 2012 conference, Alexandria, Egypt 2012.
- **Patent:** “Low Delay Dispersion Comparator – The university of Tokyo”
- **Scholarship from Egyptian Government** to Study for Ph.D.
- **Award of highest grade graduate of faculty of engineering of Assiut University 1992**, Egyptian Engineers Syndicate

Membership of Professional Bodies:

- January-2008 to present : Member of IEEE Solid State Circuit Society
- Technical Program Committee member of IEEE International Design and Test Symposium (IDT)
- Technical Program Committee member of IEEE International Symposium on Electronic System Design (ISED)
- Technical Program Committee member of IEEE JEC-ECC 2013
- April-2004 to Dec.-2006 : Student member of IEICE, Japan.
- April ~Oct-2006 : Student member of IEEE.
- July-1992 to present : Member of Engineers Syndicate, Egypt.

Training Experiences:

- Aug.-2007 to Jan.-2008: Trainer of “Developing of analytical and creative thinking skills” for the graduate students in Assiut University.

- Oct.-1995 to Nov.-2001: Trainer of “MS-Windows, MS-Office and Computer Programming” Part-time job in International Computer Center (ICC), Specialized Computer Center (SCC), IBI, Assiut, Egypt.

Computer Languages and CAD Skills:

- **CADs (EDA):** VLSI Design CADs of Cadence, Synopsis, Mentor Graphics and Tanner.
- **Circuit and Systems Simulators:** HSPICE, VHDL and MATLAB.
- **Computer languages:** C++, PASCAL, FORTRAN, BASIC, Visual Basic , and Assembly.
- **Operating Systems:** Microsoft Windows, Linux.
- **Office tools:** MS Office.

Languages:

- **Arabic:** Native
- **English:** Fluent
- **French:** Elementary
- **Japanese:** Elementary

Publications:

- [1] Nahla T. Abou-El-Kheir, **Mohamed Abbas** and Mohammed Essam Khedr, " An Adaptive Digital Background Calibration Technique using Variable Step Size LMS for Pipelined ADC", Accepted and to be appear in proceedings of IEEE CSNDSP2014, July 2014, Manchester, UK.
- [2] Nahla T. Abou-El-Kheir, **Mohamed Abbas** and Mohammed Essam Khedr, "A Fast Power Efficient Equalization-Based Digital Background Calibration Technique for Pipelined ADC, Accepted and to be appear in proceedings of IEEE MIXDES June 2014, Lublin, Poland.
- [3] S. Komatsu, T. J. Yamaguchi, **M. Abbas**, N. Khanh, J. Tandon and K. Asada, "A Flash TDC with 2.6-4.2ps Resolution Using a Group of Unbalanced CMOS Arbiters", IEICE TRANSACTIONS on Fundamentals of Electronics,

- [4] K. Khalil, **M. Abbas** and M. Abdel-Gawad, “A Low-Power Low-Delay Dispersion Comparator for High-Speed Level-Crossing ADCs”, IEEE 2nd Saudi International Electronics, Communications and Photonics Conference SIEPCPC2013, April, 2013, Riyadh, KSA
- [5] K. Khalil, **M. Abbas** and M. Abdel-Gawad, “A Low Propagation Delay Dispersion Comparator Low Cost Level-Crossing ADCs”, Proceedings of IEEE IDT2012, Dec. 2012, Doha , Qatar
- [6] M. Zanaty, R. Jansen, **M. Abbas**, A. Witvrouw, H. A. C. Tilmans, X. Rottenberg, “Influence of the nonlinearity introduced by the Casimir Force on the Harmonic behavior of MEM resonators “,The 2nd Saudi International Nanotechnology Conference (2SINC)2012, Riyadh, Saudi Arabia, November 2012
- [7] A. Khaled, M. Raouf, V. Cherman , **M. Abbas**, A. Witvrouw and De Wolf, “SiGe Double Clamped Beam MEM resonators for DNA Sensing “,The 2nd Saudi International Nanotechnology Conference (2SINC)2012, Riyadh, Saudi Arabia, November 2012
- [8] Takahiro J. Yamaguchi, Kunihiro Asada, Kiichi Niitsu, **Mohamed Abbas**, Satoshi Komatsu1, Haruo Kobayashi, Jose A. Moreira, “A New Procedure for Measuring High-Accuracy Probability Density Functions”, Accepted for Presentaion in IEEE ATS 2012, Nov. 2012, Niigata, Japan
- [9] A. Khaled, M. Raouf, V. Cherman , K. Jans , **M. Abbas**, Sh. Ebrahim, G. Bryce, P. Verheyen , A. Witvrouw and De Wolf, “Effect of the functionalization process on the performance of SiGe MEM resonators used for bio-molecular sensing”, *Microelectronics Reliability* , vol. 52 issue 9-10 September - October, 2012. p. 2272-2277
- [10] K. Khalil, **M. Abbas** and M. Abdel-Gawad, “Novel Technique for Reducing the Comparator Delay Dispersion in 45nm CMOS Technology for Level-Crossing ADCs”, IEEE ISCDG2012, Sept. 2012, Grenoble- France
- [11] M. Zanaty, R. Jansen, V. Rochus, **M. Abbas**, A. Witvrouw, H. A. C. Tilmans, X. Rottenberg, “Influence of the nonlinearity introduced by the van der Waals force on the behaviour of NEM resonators “,The 23rd Micromechanics and Microsystems Europe Workshop (MME 2012), September, 2012 Ilmenau, Germany.

- [12] T. J. Yamaguchi, S. Komatsu¹, **M Abbas**, K. Asada¹, N. Khanh and J. Tandon, "CMOS Flash TDC with 0.84 – 1.3 ps Resolution Using Standard Cells", RFIC 2012, June 2012, Montreal- Canada.
- [13] **M. Abbas** "Feedforward Compensation Technique for comparator delay dispersion for level-crossing ADCs", Journal of Electrical Science (JES) Faculty of Engineering, May 2012., Assiut University, Egypt
- [14] **M. Abbas**, T. J. Yamaguchi, Y Furukawa, S. Komatsu and K. Asada," Low Delay Dispersion Comparator for Level-Crossing ADCs", Proceedings of IEEE JECECC2012, March-2012, Alexanderia, Egypt. (Best paper of the conference)
- [15] **M. Abbas** "Toward High-Speed Level-Crossing ADCs", 8th International Conference on Electrical Engineering, Cairo, Egypt, May 2012.
- [16] **M. Abbas**, "Fault Detection and Diagnoses Methodology for Adaptive Digitally-Calibrated Pipelined ADCs", Proceedings of IEEE IDT2011, Beirut, Lebanon, Dec. 2011
- [17] **M. Abbas**, T. J. Yamaguchi, Y Furukawa, S. Komatsu and K. Asada," Novel Technique for Minimizing the Comparator Delay Dispersion in 65nm CMOS Technology", Proceedings of IEEE ICECS2011, Beirut, Lebanon, Dec. 2011
- [18] T.J. Yamaguchi, M. Soma, T. Aoki, Y. Furukawa, K. Degawa, K. Asada, **M. Abbas**, S. Komatsu," Application of a Continuous-Time Level Crossing Quantization Method for Timing Noise Measurements", ITC2011.
- [19] T. J. Yamaguchi, **M. Abbas** et al , "An Equivalent-Time and Clocked Approach for Continuous-Time Quantization", ISCAS2011, Rio de Janeiro, RJ – BRAZIL, May 2011.
- [20] **M. Abbas**, Y. Furukawa, S. Komatsu , T. J. Yamaguchi and K. Asada, "Clocked Comparator for High-Speed Applications in 65nm Technology", A-SSCC2010, China, Beijing, November 2010.
- [21] **M. Abbas**, K-T. Cheng, Y. Furukawa, S. Komatsu and K. Asada, "An Automatic Test Generation Framework for Digitally-Assisted Adaptive Equalizers in High-Speed Serial Links ", DATE2010, Germany, Dresden, March-2009.
- [22] **M. Abbas**, Y. Furukawa, S. Komatsu and K. Asada, "Signature-Based Testing for Adaptive Digitally-Calibrated Pipelined Analog-to-Digital Converters", IEEE ASICON09, Changsha, China October-2009.

- [23] **M. Abbas**, K-T. Cheng, Y. Furukawa, S. Komatsu and K. Asada, " GA-Based Test Generation for Digitally-Assisted Adaptive Equalizers in High-Speed Serial Links", IEEE EWDTS, Moscow, Russia, September-2009.
- [24] **M. Abbas**, K-T. Cheng, Y. Furukawa, S. Komatsu and K. Asada, "Signature-Based Testing for Digitally-Assisted Adaptive Equalizers in High-Speed Serial Links", IEEE European Test Symposium, ETS09, Sivilla, Spain, May-2009.
- [25] **M. Abbas**, M. Ikeda and K. Asada, " On-chip detector for single-event noise sensing with voltage scaling function" IEICE TRANS. VOL. E89-C NO.3 March 2006, pp. 370-376.
- [26] **M. Abbas**, M. Ikeda and K. Asada, " Noise Immunity Investigation of Low power Design schemes" IEICE TRANS., VOL. E89-C. NO.8 August 2006.
- [27] **M. Abbas**, M. Ikeda and K. Asada, " On-chip 8GHz Non-Periodic High-Swing Noise Detector" Proceedings of DATE 2006 , Germany, Munich, March-2006
- [28] **M. Abbas**, M. Ikeda and K. Asada, "Statistical Model for Logic Errors in CMOS Digital Circuits for Reliability-Driven Design Flow" Proceedings of DDECS06, Czech Republic, Prague, April-2006.
- [29] **M. Abbas**, M. Ikeda and K. Asada, " On-chip Detector for Non-Periodic High-Swing Noise Detection" Proc. of Int. SoC Design Conf. pp: 231-234, S. Korea, Seoul, October-2005.
- [30] **M. Abbas**, M. Ikeda and K. Asada, " On-chip Non-Periodic High-Swing Noise Detector" Proc. of 12th IEEE Int. Conf. on Electronics, Circuits and System, Tunisia , Tunis, December 2005.
- [31] **M. Abbas**, M. Ikeda and K. Asada, "Noise Effects on Performance of Low Power Design Schemes in Deep Submicron Regime" the 19th IEEE Int. Symp. On Defect and Fault Tolerance in VLSI, France, Cannes, October-2004.
- [32] **M. Abbas**, M. Ikeda and K. Asada, "On High Noise Immunity CMOS Design Scheme with Low Leakage Power Consumption", 7th Int. Conf. on Solid-State and Integrated Circuit Technology, China, Beijing, October-2004.
- [33] **M. Abbas**, M. Ikeda and K. Asada , " Statistical Evaluation of Logic Errors in Low Power Design Schemes", IEICE national Conf. 2004, Japan 2004
- [34] **M. Abbas**, M. Ikeda and K. Asada, "On-chip Detector for Non-Periodic High-Swing Noise Sensing", IEICE national Conf. 2005, Japan 20~23-Sept. 2005.
- [35] M. El-Sayed and **M. Abbas**, "Optimization of Arbitrarily Doped MINP+ as Solar Cell" Journal of Renewable Energy Vol. 14 No. 1-4-1998, pp. 141-147.

- [36] M. A. El-Sayed and **M. Abbas**, “Effect of Temperature on the Performance of MINP Solar Cell” Sharjah Solar Energy conference, United Arab Emarat, Sharjah, February- 2000.
- [37] M. A. El-Sayed and **M. Abbas**, “A Numerical Model for Arbitrarily Doped MINP Semiconductor Device as Solar Cell”, Middel East Power Conference, EGYPT, Luxor, January-1996.

Referees:

Available upon request