



(1)

Work-in-Progress: A Flexible Router Architecture for 3D NoCs

Khamis, Mostafa; Said, Mostafa; Shalaby, Ahmed

Abstract:

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(2)

A design methodology and various performance and fabrication metrics evaluation of 3D Network-on-Chip with multiplexed Through-Silicon Vias

Said, Mostafa; Shalaby, Ahmed; Mehdipour, Farhad; Biglari-Abhari, Morteza; El-Sayed, Mohamed

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